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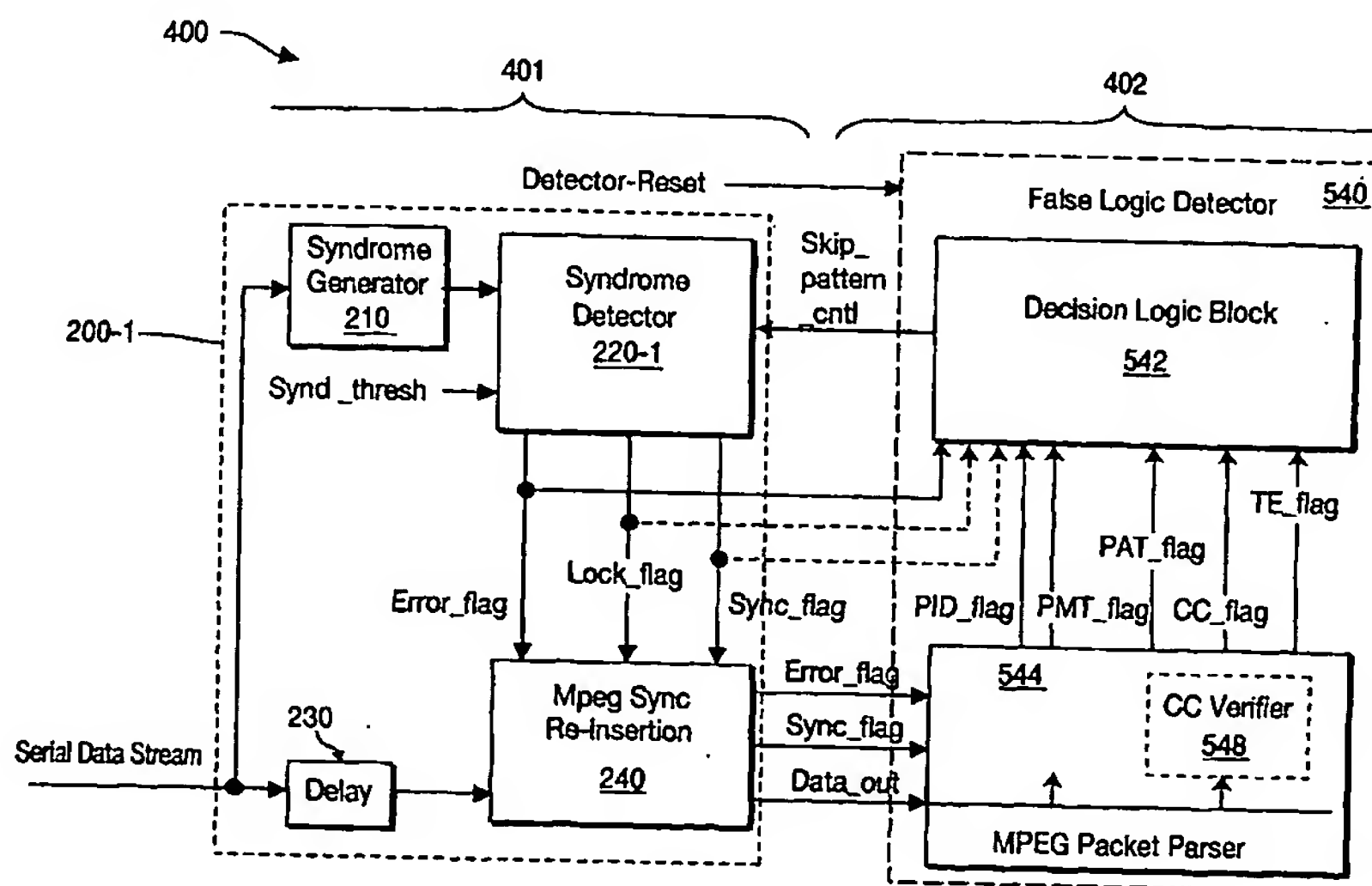
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(54) Title: METHOD AND APPARATUS FOR FALSE SYNC LOCK DETECTION IN A DIGITAL MEDIA RECEIVER



(57) Abstract: A method and apparatus (400) for identifying a false MPEG-2 packet synchronization lock condition by parsing MPEG-2 packets that may have been incorrectly delineated by a sync-byte checksum-decoder, to detect resulting anomalies. Forcing the restart of the conventional checksum-encoded sync-byte synchronization/lock process upon generating a "resync" command based upon the anomalies detected by a False Lock Detector. Reliably synchronizing and delivering the MPEG-2 stream to the receiver transport layer. The False-Lock Detector circuit compares the content of the currently identified packet header or payload position-candidates from the basis of a "synchronization lock".

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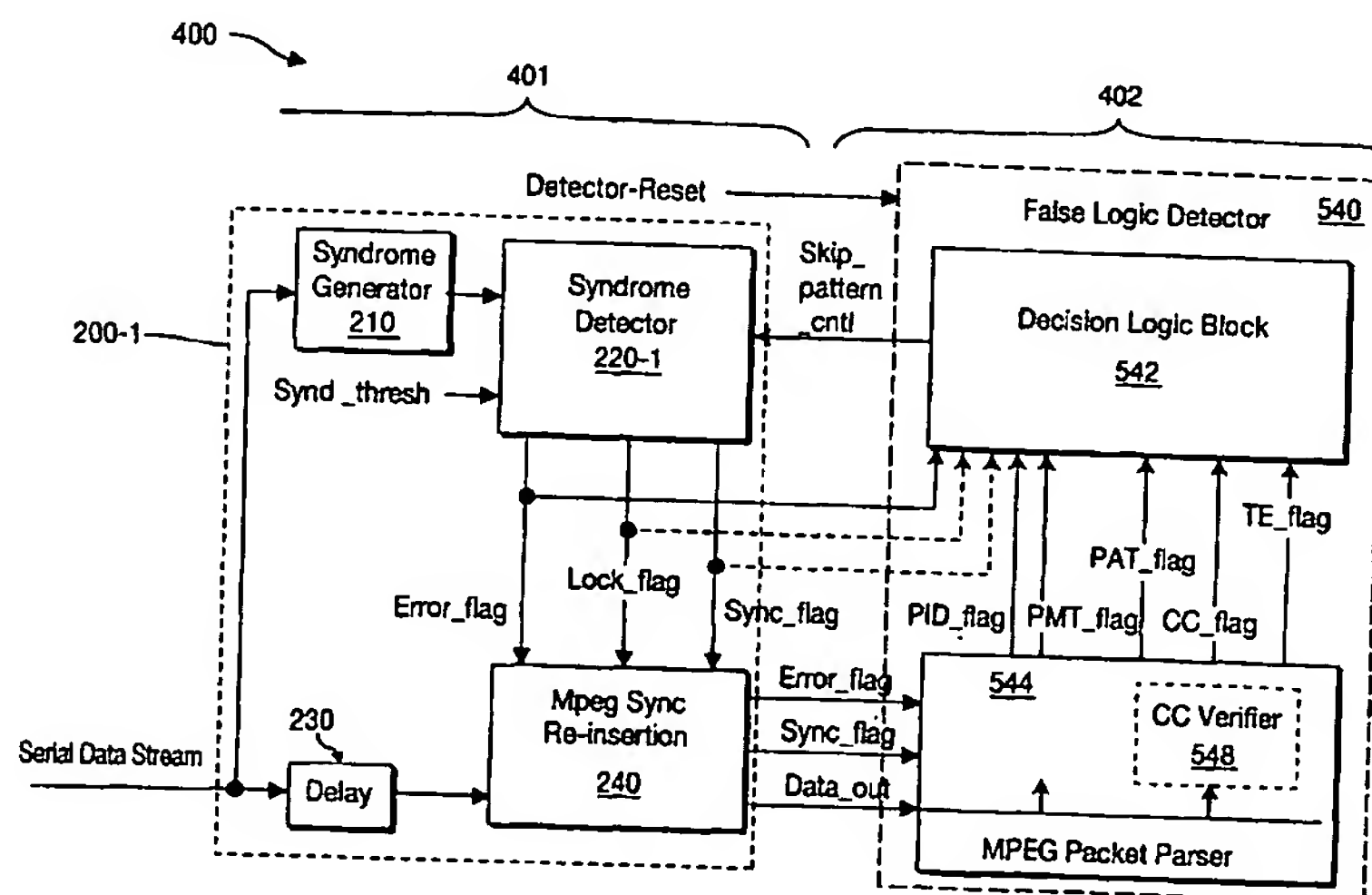
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